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Tæçà { ^ { Áo^c ] ^ cÁ& ^ ||^ } cÁÁÁÁQU T KÁ€ÈFOE

U^c] ^cAç[ |cæ\* ^ÁXUKÁ12X  
Ô[ } cã] ^ [ ^ • Ác[ cæ| Áåâ•••ã] æcã[ ]

## ÚÖKÀÈÎGÍÁY ÁÑAMÁGÍÁ ÐÁ

A97<5B=75@'85H5

Ôæ•^KÁVUËJGÁÙ { æ||ÁU ^ c|â } ^ÁÚ|æ•ç&ÁÚæ&Íæ\*^  
Ú||æ!âc^K Ô|||:

F



O GÍ »ÔŒ { àâ^ }c V^ { ] ^{æc} ^{ç} } ^{••} [ c @ ^ { , à•^ } ] [ c ^ {å} ]

Parameter	mbo	a e	Jnit
Input Voltage	V <sub>i</sub>	35	V
Thermal Resistance from Junction to Ambient	R <sub>θJA</sub>	166.7	°C/W
Operating Junction Temperature Range	T <sub>OPR</sub>	-25~+125	°C
Storage Temperature Range	T <sub>STG</sub>	-65~+150	°C

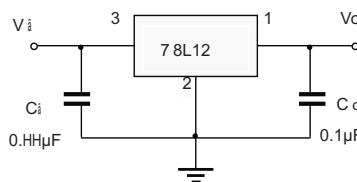
9@97HF=75@7<5F57H9F=GH=7G'5H'GD97=-:98'J=FH15@>IB7H'CB'H9AD9F5H'F9T

ÇXåMFJXÉQ|M|€ { ØÈÖåM€ÈHH~ØÈÈÔ|M€ÈF~ØÈA~}|^••Á|[c@!|, ä•^Ä•|^&ä-ä^åÅDÅ

Parameter	mbo	e t on ition		in		a	Junit
Output voltage	Vo			25°C	11.5	12	12.5 V
		14V≤Vi≤27V, Io=1mA-40mA		0-125°C	11.4	12	12.6 V
		Io=1mA-70mA			11.4	12	12.6 V
Load Regulation	△Vo	Io=1mA-100mA		25°C		22	100 mV
		Io=1mA-40mA		25°C		13	50 mV
Line regulation	△Vo	14.5V≤Vi≤27V		25°C		55	250 mV
		16V≤Vi≤27V		25°C		49	200 mV
Quiescent Current	Iq			25°C		4.3	6.5 mA
Quiescent Current Change	△Iq	16V≤Vi≤27V		0-125°C		1.5	mA
	△Iq	1mA≤Io≤40mA		0-125°C		0.1	mA
Output Noise Voltage	V <sub>N</sub>	10Hz≤f≤100KHz		25°C		70	μV/Vo
Ripple Rejection	RR	15V≤Vi≤25V, f=120Hz		0-125°C	37	42	dB
Dropout Voltage	Vd			25°C		1.7	V

\* Pul e e .

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Note : Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the power pins.

